



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Anatoliy V. Tsyrchanovich
Assignee: ZiLOG, Inc.
Title: "Circuit and Method for Reducing East-West Geometry Mismatch
Between the Top and Bottom of a Raster Display"
Serial No.: 10/820,237 Filed: April 5, 2004
Examiner: Angela M. Lie Group Art Unit: 2163
Atty. Doc. No.: ZIL-519-1C

July 20, 2007

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**NOTICE OF APPEAL FROM THE PRIMARY EXAMINER TO THE
BOARD OF PATENT APPEALS AND INTERFERENCES**

Applicant hereby appeals to the Board of Patent Appeals and Interferences from the decision of the Primary Examiner dated April 25, 2007, finally rejecting claims 55 and 60-74 of the above-referenced application. A Request for Pre-Appeal Conference and Remarks in Support of the Pre-appeal Brief Request for Review are also submitted.

A check is enclosed for payment of the \$500 Notice of Appeal fee required under 37 CFR §41.20(b)(1).

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

By

Darien K. Wallace
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Date of Deposit: July 20, 2007

Respectfully submitted,

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